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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/724,498	12/01/2003	Donald C. Abbott	28098.1	9998

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EXAMINER	
NGUYEN, DILINH P	
ART UNIT	**PAPER NUMBER**
2814	

DATE MAILED: 06/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/724,498	ABBOTT ET AL.	
	Examiner	Art Unit	
	DiLinh Nguyen	2814	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *20 April 2004*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *17-22* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *17-22* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date *12/01/03*.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

Claim Objections

Claims 17 and 22 are objected to because of the following informalities:

Line 1, claim 17, replace "thesteps" with –the steps—.

Line 13, claim 22, replace "ptimize" with –optimize--.

Appropriate correction is required.

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

2. Claims 17-21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Abbott (U.S. Pat. 6245448) in view of Huang et al. (U.S. Pat. 5994767) and further in view of Grunwald et al. (U.S. Pat. 3819497).

- Regarding claims 17 and 19, Abbott discloses a method for fabricating a leadframe comprising the steps of:

  providing a copper leadframe 34 having a mount pad 14 for an integrated circuit chip and a plurality of lead segments 16 having their first end near the mount pad and their second end remote from the mount pad;

  providing a copper leadframe 28;

  depositing a first layer of nickel 36 onto the copper;

  electroplating a layer comprising an alloy of nickel and palladium 38;

electroplating a second layer of nickel 40, thereby adapting the lead segments for mechanical bending;

electroplating a layer of palladium 42 (fig. 3, column 3, lines 13-17).

Abbott fails to explicitly show gold selectively plated on segments of the leadframe intended for solder attachment and cleaning the leadframe in alkaline soak; activating the leadframe by immersing the leadframe into an acid solution and immersing the leadframe into an electrolytic nickel plating solution.

However, Huang et al. disclose a method for fabricating a leadframe comprising the steps of: providing a copper leadframe 30 having a mount pad for an integrated circuit chip 40 and a plurality of lead segments 30 having their first end near the mounting pad and their second end remote from the mount pad; a copper layer 32; a nickel layer 34; a palladium layer 54 and a gold layer on the lead frame (cover fig.), either over the entire leadframe or selectively only over specific portions of the leadframe (column 2, lines 31-34) for the purpose of making solder connections.

Grunwald et al. disclose a method for fabricating a copper sheet comprising the steps of:

providing a sheet made of copper (column 2, lines 65-66);

cleaning the copper sheet in alkaline soak and electro-cleaning solutions (column 3, lines 4-15);

activating the surface of the copper sheet by immersing the copper sheet into an acid solution (column 3, lines 43-45 and 48-50); and

immersing the activated the copper sheet into a chromating solution including chromic acid and an activator (column 3, lines 51-56) to improve the adhesion for the semiconductor package and reduce complexity of implementation (column 2, lines 53-57).

Therefore, it would have been obvious to one of ordinary skill in the art to modify the process step of Abbott by plating gold selectively on segments of the leadframe and activating the surface of the lead frame by immersing the lead frame into an acid solution, as taught by Huang et al. and Grunwald et al., for the purpose of making solder connection and in order to improve the adhesion for the semiconductor package and reduce complexity of implementation.

- Regarding claim 18, it would have been obvious in the art wherein the gold plating of Huang et al. is performed electrolytically or electrolessly.
- Regarding claim 20, Grunwald et al. disclose the process steps are obviously executed in sequency without time delays, yet including intermediate rinsing steps.
- Regarding claim 21, Grunwald et al. disclose the acid solution may be sulfuric acid and hydrochloric acid (column 6, lines 50-55 and column 7, lines 15-17).

3. Claim 22 is rejected under 35 U.S.C. 103(a) as being unpatentable over Huang et al. (U.S. Pat. 5994767) in view of Grunwald et al. (U.S. Pat. 3819497).

Huang et al. (cover fig.) disclose a method for fabricating a leadframe comprising the steps of: providing a copper leadframe 30 having a mount pad for an integrated circuit chip 40 and a plurality of lead segments 30 having their first end near the

mounting pad and their second end remote from the mount pad; a copper layer 32; a nickel layer 34; a palladium layer 54 and a gold layer on the lead frame, either over the entire leadframe or selectively only over specific portions of the leadframe (column 2, lines 31-34) for the purpose of making solder connections.

Huang et al. fail to disclose the step of cleaning the leadframe in alkaline soak; activating the leadframe by immersing the leadframe into an acid solution and immersing the leadframe into an electrolytic nickel plating solution.

Grunwald et al. disclose a method for fabricating a copper sheet comprising the steps of:

providing a sheet made of copper (column 2, lines 65-66);

cleaning the copper sheet in alkaline soak and electro-cleaning solutions (column 3, lines 4-15);

activating the surface of the copper sheet by immersing the copper sheet into an acid solution (column 3, lines 43-45 and 48-50); and

immersing the activated the copper sheet into a chromating solution including chromic acid and an activator (column 3, lines 51-56). Therefore, it would have been obvious to one having ordinary skill in the art to modify the process step of Huang et al. by cleaning the lead frame in alkaline soak and electro-cleaning solutions; activating the surface of the lead frame by immersing the lead frame into an acid solution, as taught by Grunwald et al., in order to improve the adhesion for the semiconductor package and reduce complexity of implementation (column 2, lines 53-57).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DiLinh Nguyen whose telephone number is (571) 272-1712. The examiner can normally be reached on 8:00AM - 6:00PM (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DLN

HOAI PHAM
PRIMARY EXAMINER